

ACHARYA NAGARJUNA UNIVERSITY

REVISED Time-Table for M.TECH. (CR) (2nd Semester) - AUGUST – 2016 Examinations

TIME :: 10.00 A.M. to 1.00 PM.

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MAX. MARKS : 70 (2011 & 2013)

MAX. MARKS : 60 (R-2015)

Day & Date	Computer Science & Technology	Communication Engineering & Signal Processing	CAD/CAM/MD	Structural Engineering	Computer Science Engineering / Digital Image Processing	Power Systems Engg.	VLSI & Embedded Systems Design
26-08-2016 Friday	CST-521(CR) Distributed Operating Systems CST – 514 (CR) Computer Networks	CESP – 521 / 514 (CR) Real Time Signal Processing	MT/ME/CC-521 / 514 (CR) Computer Integrated Manufacturing MT/ME/MD-521/514 Advanced Mechanisms Design	MCE/SE – 201 / 514 (CR) Finite Element Analysis of Structures	CSE – 521 / 514 (CR) Data Engineering CSE – 521 / 514 (DIP) Advanced Image & Video Processing	MT/PSE - 521 / 514 (CR) Flexible AC Transmission Systems	VES – 521 / 514 Low Power VLSI Design
29-08-2016 Monday	CST – 522/515 (CR) Data Engineering	CESP – 522 / 515 (CR) Multirate Systems and Filter Banks	MT/ME/CC – 522 / 515 (CR) Robotics MT/ME/MD – 515 Mechanics of Fracture and Fatigue MT/ME/MD – 522 Robotics	MCE/SE – 202 / 515 (CR) Stability of Structures	CSE – 522 / 515 (CR) Design Analysis of Algorithms CSE – 522 / 515 (DIP) Pattern Recognition & Analysis	MT/PSE - 522 / 515(CR) Power System Stability	VES – 522 / 515 Algorithms for VLSI Design Automation
31-08-2016 Wednesday	CST – 523/516 (CR) Web Technologies	CESP – 523 / 516 (CR) Wireless Communication	MT/ME/CC/MD – 523 / 516 (CR) Optimization Techniques	MCE/SE – 203 / 516 (CR) Theory of Plates and Shells	MT/CS - 523 (CR) Advanced Computing CSE – 523 / 516 (DIP) Multimedia Systems CSE-516 (CR) Linux Programming	MT/PSE - 523 / 516 (CR) Real Time Control of Power Systems	VES – 523 / 516 Embedded System Design
02-09-2016 Friday	CST-524(CR) (Ele-IV) (A) Object Oriented Analysis & Design CST-611E Software Engineering	CESP-524 (CR) (Ele-I) (1) Embedded Systems (2) Pattern Recognition (3) Random Processing & Information Theory MT/CESP - 620 (CR) (Ele-I) Embedded Systems	MT/ME/CC-524A (CR) (Ele-IV) Advanced Mechanisms Design MT/ME/MD-524 (Ele-IV)-A Tool Design MT/ME/CC/MD-524-C Quality Engineering MT/ME/CC – 618 Mechatronics MT/ME/MD – 613 Robotic Engineering	MCE/SE/204 (CR) (Ele-IV) (A) Earthquake Resistant Design of Structures (B) Disaster Management (C) Ground Improvement Techniques MCE/SE – 622 Ground Improvement Techniques MCE/SE – 616 Experimental Stress Analysis and Motion Management MCE/SE – 620 Earthquake Resistant Design of Structures	CSE-524 (Ele-I) (CR) CSE/DIP-524 (A) Bioinformatics CSE/DIP-524(B) Evolutionary Algorithms CSE/DIP-524(C) Machine Learning CSE-524(D) Soft Computing DIP-524 (D) Data Engineering CSE – 615 Network & Internet Security CSE – 611 – Object Oriented Software Engineering DIP-621E - Data Engg.	MT/PSE-524 (CR) 1. HVDC Transmission Systems 2. Power Quality 3. Digital Control Systems MT/PSE - 621 Power Quality MT/PSE - 620 HVDC Transmission Systems	VES – 524 (Ele. -I) 1) Modeling and Synthesis with Verilog HDL 2) VHDL Modeling of Digital Systems 3) VLSI Fabrication Technology Banks VES – 621E VHDL Modeling of Digital Systems

