

ACHARYA NAGARJUNA UNIVERSITY
REVISED TIME TABLE FOR M.TECH. (2nd SEMESTER) REGULAR EXAMINATIONS – JULY – 2019

TIME :: 10.00 A.M. to 1.00 PM.

MAX. MARKS : 60 (R-2015)

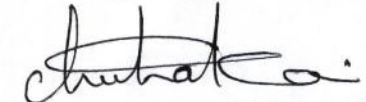
| Day & Date | Communication Engg. & Signal Processing | CAD / CAM / MD | Structural Engineering | Computer Science & Engineering / Digital Image Processing | Power System Engineering | VLSI & Embedded Systems Design |
|-------------------------|---|--|--|---|--|---|
| 01-07-2019 Monday | CESP – 514 (R-15) Real Time Signal Processing | MT/ME/CC-514 (R-15) Computer Integrated Manufacturing MT/ME/MD-514 (R-15) Advanced Mechanisms Design | MCE/SE – 514 (R-15) Finite Element Analysis of Structures | CSE – 514 (R-15) Data Engineering CSE – 514 (DIP) (R-15) Advanced Image & Video Processing | MT/PSE - 514 (R-15) Flexible AC Transmission Systems | VES – 514 (R-15) Low Power VLSI Design |
| 03-07-2019 Wednesday | CESP – 515 (R-15) Multirate Systems and Filter Banks | MT/ME/CC–515 (R-15) Robotics MT/ME/MD–515 (R-15) Mechanics of Fracture and Fatigue | MCE/SE – 515 (R-15) Stability of Structures | CSE –515 (R-15) Design Analysis of Algorithms CSE –515 (DIP) (R-15) Pattern Recognition & Analysis | MT/PSE - 515(R-15) Power System Stability | VES –515 (R-15) Algorithms for VLSI Design Automation |
| 05-07-2019 Friday | CESP – 516 (R-15) Wireless Communication | MT/ME/CC/MD – 516 (R-15) Optimization Techniques | MCE/SE – 516 (R-15) Theory of Plates and Shells | CSE – 516 (DIP) (R-15) Multimedia Systems CSE-516 (R-15) Linux Programming | MT/PSE - 516 (R-15) Real Time Control of Power Systems | VES –516 (R-15) Embedded System Design |
| 08-07-2019 Monday | CESP-623 (R-15) Satellite Communication Systems CESP-626 (R-15) Fuzzy Techniques | MT/ME/CC – 614 (R-15) Design for Manufacturing MT/ME/MD-621 (R-15) Mechanics of Composite Materials MT/ME/CC – 618 (R-15) Mechatronics | MCE/SE – 622 (R-15) Ground Improvement Techniques MCE/SE – 620 (R-15) Earthquake Resistant Design of Structures | DIP-621 (R-15) Data Engineering CSE-621 (R-15) Wireless Networks | MT/PSE - 620 (R-15) HVDC Transmission Systems MT/PSE - 611 (R-15) Operation Research | VES –621 (R-15) VHDL Modeling of Digital Systems |
| 10-07-2019 Wednesday | CESP-625 (R-15) Telecommunication Switching Systems | MT/ME/MD – 624 (R-15) Design of Experiments | MCE/SE – 625 (R-15) Design of Prestresses Concrete Structures MCE/SE – 624 (R-15) Composite construction | DIP-613 (R-15) Artificial Neural Networks CSE-622 (R-15) Compiler Design | MT/PSE - 623 (R-15) Electrical Distribution Systems MT/PSE - 617 (R-15) EHV AC Transmission Systems | VES –625 (R-15) Design of Fault Tolerant Systems |

(P.T.O.)

| Day & Date | Communication Engg. & Signal Processing | CAD / CAM / MD | Structural Engineering | Computer Science & Engineering / Digital Image Processing | Power System Engineering | VLSI & Embedded Systems Design |
|------------------------|--|---|--|--|--------------------------------------|--|
| 12-07-2019 Friday | CESP-628 (R-15) OFDM for Wireless Communication Systems | MT/ME/CC – 627 (R-15) Fluidics & Control Systems | MCE/SE – 626 (R-15) Repair and Rehabilitation of Structures | CSE-625 (R-15) Distributed Systems CSE-626 (E) Advanced Computer Architecture | MT/PSE - 626 (R-15) AI Techniques | VES –626 (R-15) ARM – CORTEX Processor Architecture and Programming |
| 13-07-2019 Saturday | CESP-620 (R-15) Embedded Systems | MT/ME/CC/MD-625 (R-15) Reliability Engineering | | DIP-626 (R-15) Embedded Systems | MT/PSE - 621 (R-15) Power Quality | |

Dated: 22-06-2019

Nagarjuna Nagar.


CONTROLLER OF EXAMINATIONS

Note: If any discrepancy noticed in the above time table, please inform to the undersigned immediately.

To

The Principals of all the Engg. Colleges offering M.Tech. Courses, A.N.U., with a request to send the question paper requirement to the
Co-Ordinator, P.G. Examinations, A.N.U.

Copies to the Co-Ordinator Squads, ANU. Co-ordinator, P.G. Examinations, A.N.U,
P.A. to Vice-Chancellor / Rector / Registrar, A.N.U.